

FIG. 1

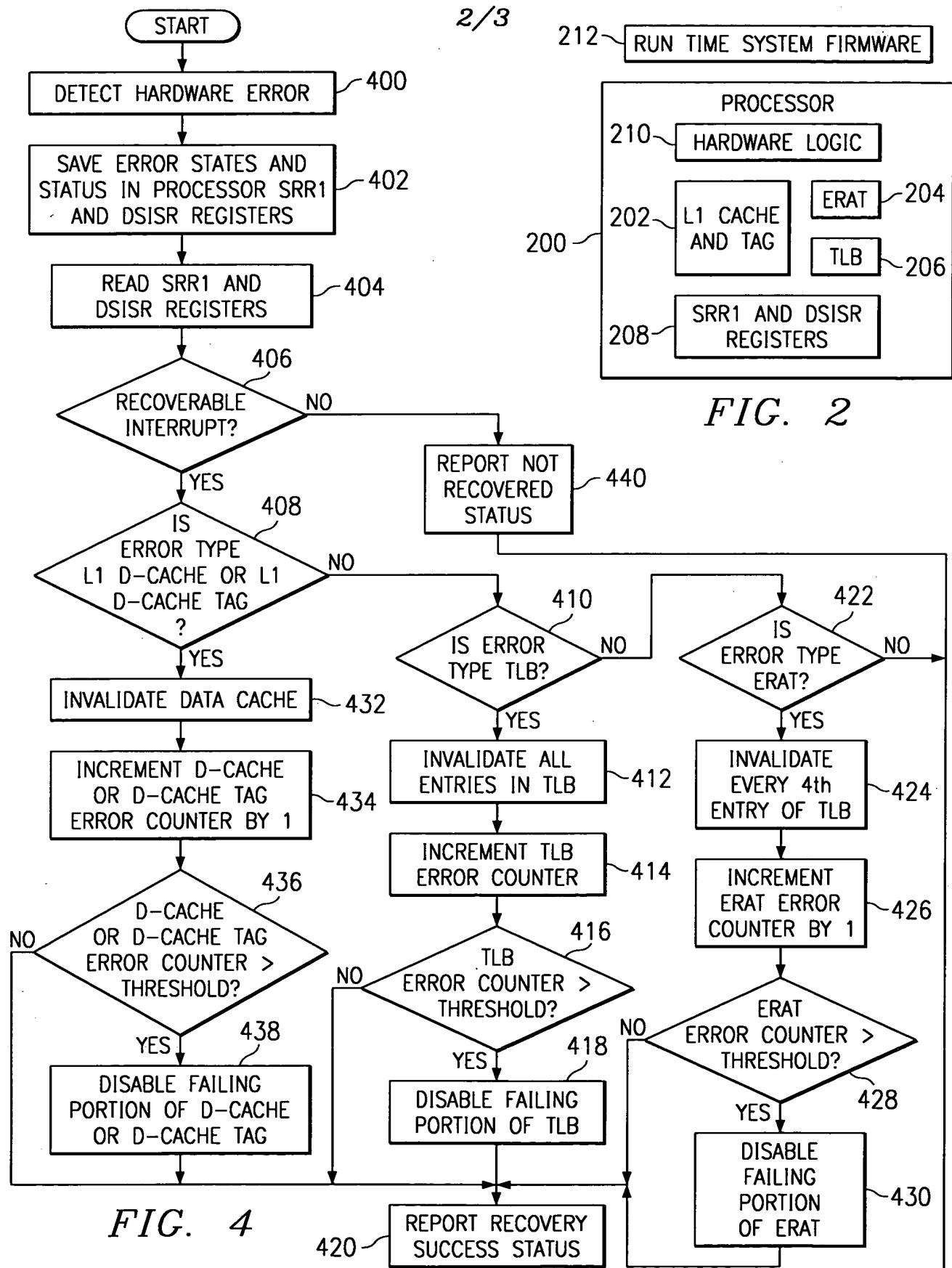


FIG. 2

FIG. 4

300

FIG. 3

| ERROR DESCRIPTION | HARDWARE INDICATION | FIRMWARE RECOVERY | LOG RETURN SEVERITY | LOG RETURN RTAS DISPOSITION |
|---|---|--|--|-----------------------------|
| - LOST HW STATES | SRR1[RI] = 0 | NONE | FATAL (ERROR NOT SYNCHRONOUS TO CURRENT CONTEXT) | NOT RECOVERED |
| - D-CACHE PARITY - D-CACHE TAG PARITY UNDER THRESHOLD | SRR1[RI] = 1 AND SRR1[4:3] = 1 AND DSISR[18/19] = 1 | INVALIDATE D-CACHE | WARNING | FULLY RECOVERED |
| - D-CACHE PARITY - D-CACHE TAG PARITY OVER THRESHOLD | SRR1[RI] = 1 AND SRR1[4:3] = 1 AND DSISR[18/19] = 1 | - INVALIDATE D-CACHE - DISABLE FAILING PORTION OF D-CACHE | WARNING | FULLY RECOVERED |
| - L/S TLB PARITY - L/S D-ERAT PARITY UNDER THRESHOLD | SRR1[RI] = 1 AND SRR1[4:3] = 1 AND DSISR[20/21] = 1 | INVALIDATE TLB ALL | WARNING | FULLY RECOVERED |
| - L/S TLB PARITY - L/S D-ERAT PARITY OVER THRESHOLD | SRR1[RI] = 1 AND SRR1[4:3] = 1 AND DSISR[20/21] = 1 | INVALIDATE TLB ALL, DISABLE FAILING PORTION OF ERAT OR TLB | WARNING | FULLY RECOVERED |
| - IFETCH TLB PARITY UNDER THRESHOLD | SRR1[RI] = 1 AND SRR1[44:45] = 10 | INVALIDATE TLB ALL | WARNING | FULLY RECOVERED |
| - IFETCH TLB PARITY OVER THRESHOLD | SRR1[RI] = 1 AND SRR1[44:45] = 10 | INVALIDATE TLB ALL, DISABLE FAILING PORTION OF TLB | WARNING | FULLY RECOVERED |